Visible Electroluminescence from Stain-Etched Porous Si Diodes

J. Xu, Student Member, IEEE, and A. J. Steckl, Senior Member, IEEE

Abstract—Visible electroluminescence (EL) from stain-etched porous silicon (PoSi) films is presented. The PoSi thin layers (~200 nm) were obtained by stain-etching of B-doped 6–16 Ω-cm (100) crystalline Si in a HF:HNO₃:H₂O (1:3:5) solution. Indium tin oxide (ITO) films of ~2500 Å were used to form a Schottky contact. Visible EL was observed at room temperature from the diode under forward bias. EL onset bias as low as 3 mA/cm² was measured. The EL, with an emission peak at ~640 nm, is similar to the photoluminescence under UV excitation, indicating the same luminescent centers. This result demonstrates a promising and simple technique for the fabrication of PoSi-based light emitting diodes and flat panel display devices.

I. INTRODUCTION

The report of strong visible photoluminescence (PL) at room temperature obtained from porous silicon [1] (PoSi) has triggered great interest in achieving Si-based optoelectronic devices in spite of the lack of complete understanding [2] of this phenomenon. A few authors have reported visible electroluminescence (EL) from solid-state Schottky contacts between a PoSi layer obtained from anodically etched crystalline Si (c-Si) and thin films of Au [3–6], Al [7], indium tin oxide (ITO) [8], [9], or conducting polymer [10], [11]. EL was also reported from an anodized PoSi p-n homojunction [12] and a SiC/PoSi heterojunction [13, [14]. The available device characteristics of anodized PoSi light emitting diodes (LED's) reported to date are summarized in Table I. The table indicates the device’s structure, the thickness of the PoSi layer, the leakage current density at a given reverse bias voltage, the current rectification ratio obtained between given forward and reverse bias voltages, the current density (bias) required for onset of EL (both visually observable and photodetector measured), and the peak wavelength of the EL spectrum.

Photoluminescent PoSi can also be easily obtained by purely chemical etching (stain-etching) [15] of c-Si or poly crystalline Si in HF:HNO₃-based solutions. While the underlying mechanism of PoSi formation is the same for both techniques, the stain-etching process is much simpler and is, therefore, easier to be integrated into existing Si technology. More importantly, only stain-etching has the demonstrated capability [16] to achieve sub-micron luminescing PoSi patterns embedded in conventional Si. This capability is very important for monolithic integration of optically active Si components onto a Si substrate. In addition, for application in flat panel display devices, stain-etching has the advantage to easily produce photoluminescent poly-PoSi films on quartz and glass [17], without the need of an electrode as required in anodization. However, to date there has been a scarcity of information regarding EL in stain-etched PoSi. In this letter we report visible EL from stain-etched thin PoSi films, with a spectrum very similar to that of the PL.

II. EXPERIMENTAL

The starting material was B-doped CZ (100) Si substrates (6–16 Ω-cm). An aluminum film was sputtered onto the back side of the wafer and subsequently annealed to provide an ohmic contact prior to PoSi formation. The front side of the sample was then rendered porous by stain etching in a mixture of HF:HNO₃:H₂O (1:3:5 volume) for 1 min beyond the incubation time [16]. At the same time Al was protected by photoresist. Stain-etching was performed in ambient light at room temperature. Immediately after stain-etching, the sample was rinsed in deionized water and blown dry with nitrogen. An indium tin oxide (ITO, 90% In₂O₃ + 10% SnO₂) thin film of ~2500 Å was deposited onto the PoSi samples through a shadow mask resulting in transparent circular electrodes with an area of either ~0.08 cm² or ~0.26 cm².

III. RESULTS AND DISCUSSION

The PoSi film obtained was very smooth, as revealed by optical and scanning electron microscopy (SEM), and showed a shiny, mirror-like dark-blue interference color. No obvious modification of the surface morphology (such as bubbling) was observed during the stain-etch. Cross-section SEM analysis of

<table>
<thead>
<tr>
<th>Device Structure</th>
<th>PoSi Layer Thickness (nm)</th>
<th>Leakage Current Density (mA/cm²)</th>
<th>Rectification Ratio</th>
<th>Voltage (V)</th>
<th>Photoluminescence (nm)</th>
<th>EL Peak Wavelength (nm)</th>
<th>Ref. No.</th>
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</thead>
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<td>n-PoSi</td>
<td>7</td>
<td>1.5</td>
<td>1</td>
<td>1.0</td>
<td>500</td>
<td>600</td>
<td>2</td>
</tr>
<tr>
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<td>1.0</td>
<td>1</td>
<td>1.0</td>
<td>500</td>
<td>600</td>
<td>2</td>
</tr>
<tr>
<td>p-PoSi</td>
<td>3</td>
<td>1.0</td>
<td>1</td>
<td>1.0</td>
<td>500</td>
<td>600</td>
<td>2</td>
</tr>
<tr>
<td>p-PoSi</td>
<td>4</td>
<td>2.0</td>
<td>3</td>
<td>1.0</td>
<td>500</td>
<td>600</td>
<td>2</td>
</tr>
<tr>
<td>p-PoSi</td>
<td>5</td>
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<td>3</td>
<td>1.0</td>
<td>500</td>
<td>600</td>
<td>2</td>
</tr>
<tr>
<td>Silicon/PoSi</td>
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<td>3</td>
<td>1.0</td>
<td>500</td>
<td>600</td>
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the samples indicates that the PoSi film has a uniform thickness of about 200 nm, with the presence of vertical pores (with diameters of 10-20 nm) existing through the entire thickness of the PoSi layer. The device structure is illustrated in the inset of Fig. 1, implementing a Schottky diode between the ITO and the PoSi film. The diode exhibited strongly rectifying I-V characteristics as shown in Fig. 1. The diode turned on at a forward bias of around 1.5 V and had a reverse breakdown voltage in excess of 50 V. Typical leakage current density of the diodes at a reverse bias of −10 V was ~538 nA/cm², which is close to the lowest reported value [7]. The diodes have a rectifying ratio of 1.6 × 10⁵ at ±15 V, which is the highest value reported to date (see Table I). Visible EL was readily observed with the naked eye in a dark background when a forward current larger than 10 mA/cm² was applied. No EL was produced under reverse bias. The observed EL color (reddish-orange) is very similar to that of PL under UV (365 nm) excitation. The EL intensity, measured with a DR-1 A Digital Radiometer from Gamma Scientific, increases monotonically with current. The EL intensity is stable and reproducible. An example is contained in Fig. 2, where EL intensity is shown as a function of diode current. The two sets of data displayed are taken from the same diode, with two hours of electrical aging under continuous dc current of 10 mA (40 mA/cm²) between the two sets of measurements. During the aging period, the emission was continuously monitored at a current of 10 mA and a variation of less than 5% was observed. From Fig. 2, the EL intensity increases with a power law dependence on the current (EL ∝ J⁰.⁵), with a power of 2 to 3. This behavior is similar to that reported by Maruska et al. for anodized ITO/PoSi diodes [18]. However, further work is needed to understand this nonlinear dependence, especially at low bias where m~3 was observed. The lowest EL onset observed with a photodetector was obtained at ~3 mA/cm². The inset in Fig. 3 is an EL image from a diode under a forward current density of ~40 mA/cm². It can be seen that uniform light emission is produced from the area where the PoSi is in contact with the ITO electrode, in other words where electron injection occurs under forward bias. No EL is observed when the ITO is deposited directly on c-Si.

PL and EL spectra were recorded using a 0.5 m monochromator with a photomultiplier. The PL was excited with a filtered UV 365 nm line from a 100 W Hg lamp. EL was measured at a forward dc bias of 190 mA/cm². Shown in Fig. 3 are examples of typical PL and EL spectra. The PL, taken from PoSi through the 95% transparent electrode, has the familiar spectrum of stain-etched PoSi [16] peaked at around 650 nm with a full width at half-maximum (FWHM) of ~100 nm. The EL has a spectrum very similar to that of the PL with a peak at 640 nm. The similarity in emission spectra strongly suggests that the EL from the PoSi layer is caused by the same luminescent center as that of the PL. This is in strong contradiction to the only previous report of EL from chemically etched Si [19]. In that case, the EL and PL peak positions are separated by more than 190 nm and it was suggested that a different mechanism was responsible for the EL [19].

The characteristics of the stain-etched PoSi LED reported here are compared in Table I with previously published anodized PoSi LED’s. It is clear from the comparison that by employing a thin and uniform stain-etched PoSi layer, the device characteristics were significantly improved in areas such as leakage current, rectifying ratio, and EL onset current. However, the intensity and efficiency of EL from stain-etched (and anodized PoSi) still need to be significantly improved in order for device applications to become practical. EL has also been obtained from stain-etched PoSi Schottky diodes using Au thin films (~100 Å). The results are similar to those of the ITO/PoSi diodes described above.
IV. CONCLUSION

In summary, EL has been obtained from stain-etched PoSi thin film diodes using ITO and Au Schottky contacts. The PoSi film used in this work of only about 200 nm is the thinnest ever reported for a PoSi LED. The device has superior electrical characteristics and achieved the lowest EL onset current. Since the stain-etch process is much simpler than anodization and can be used to form sub-micron luminescent PoSi patterns and to produce luminescing poly-PoSi films on quartz and glass, these results demonstrate a very promising and advantageous technique for fabrication of PoSi-based LED’s and poly-PoSi-based electroluminescent flat panel displays.

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REFERENCES